

HIGH LEVEL DIGITAL COMMUNICATION TRAINERS

HDCT-8000



Description : Curriculum Outlines:

1. Design and implementation of CVSD modem.
2. Design and implementation of QPSK and QAM modem.
3. Design and implementation of GMSK and OFDM modem.
4. Design and implementation of Block and Convolution encoder and decoder.

Features : Curriculum Objectives:

1. To understand the theory of high level digital communication system.
2. Design and implementation ability training of high level digital modem.
3. To understand the applications of balanced modulator circuit.
4. To understand the applications of high level digital modem.

Specification : Module One: CVSD Modulation and Demodulation (HDCT-8000-01)

Chapter 1: CVSD Modulator

Chapter 2: CVSD Demodulator

Module Two: QPSK Modulation and Demodulation (HDCT-8000-02)

Chapter 3 QPSK Modulator

Chapter 4 QPSK Demodulator

Module Three: QAM Modulation and Demodulation (HDCT-8000-03)

Chapter 5 QAM Modulator

Chapter 6 QAM Demodulator

Module Four: GMSK Modulation and Demodulation (HDCT-8000-04)

Chapter 7 GMSK Modulator

Chapter 8 GMSK Demodulator

Module Five: OFDM Modulation and Demodulation (HDCS-8000-05)

Chapter 9 OFDM Modulator

Chapter 10 OFDM Demodulator

Module Six: Block Encoder and Decoder (HDCS-8000-06)

Chapter 11 Block Encoder

Chapter 12 Block Decoder

Module Seven: Convolution Encoder and Decoder (HDCS-8000-07)

Chapter 13 Convolution Encoder

Chapter 14 Convolution Decoder